

THESES AWARDED

Ph.D. (18)

Arshad Hussain

High-Resolution Passive and Active-Passive Switched-Capacitor Delta-Sigma Modulator Design Techniques in Nanoscale CMOS, 2017

Assistant Professor, Quaid-i-Azam University, Islamabad, Pakistan

Lei WANG

Design and Implementation of a Three-Phase Three-Wire Thyristor Controlled LC-Coupling Hybrid Active Power Filter with Low DC-link Voltage and Wide Compensation Range, 2017

Post-Doc, University of Macau, Macao

Tianlan CHEN

Thermal Digital Microfluidic Devices for Rapid DNA Analysis, 2016

Research Assistant, University of Macau, Macao

Ka-Meng LEI

Handheld CMOS-Based NMR Devices for Biological/Chemical Diagnosis, 2016

Macao-fellow, University of Macau, Macao

Chio-In IEONG

Low-Power CMOS Processors Design for ECG QRS Wave Detection and Data Compression, 2016

Senior Engineer, Hisilicon, China

Jianyu ZHONG

High-resolution Power-efficient SAR-Type ADCs, 2016

Post-Doc, University of Macau, Macao

Ximei CHEN

Channel Characteristics and Communication Performance of Galvanic Coupling Human Body Communication, 2016

Jie GAO

Electronic-Automated Intelligent Digital Microfluidic System and its Applications to DNA Amplification, 2015

Laboratory Technician, University of Macau, Macao

Tawfiq AMIN

Analysis and Design of Power-Efficient Voltage-Controlled Oscillators for Wireless Applications in Nanoscale CMOS, 2015

Assistant Professor, Military Institute of Science & Technology (MIST), Dhaka, Bangladesh

Yaohua ZHAO

Low-Power, High-Linearity and Area-Efficient Switched-Capacitor Filters Design Techniques in Nanoscale CMOS, 2015

Senior Design Engineer, Mediatek, Singapore

Fujian LIN

Advanced Circuit Design Techniques for Low-Power Wide-band Wireless Receivers in Nanoscale CMOS, 2014

Senior Design Engineer, Mediatek, Singapore

Zhicheng LIN

Ultra-Low-Power and Ultra-Low-Cost Short-Range Wireless Receivers in Nanoscale CMOS, 2014

Senior Design Engineer, Qualcomm, USA

Zushu YAN

Systematic Design and Implementation of Single-Stage and Multi-Stage CMOS Amplifiers, 2014

Senior Design Engineer, Qualcomm, USA

Ka-Fai UN

New Architectures and Circuit Design Techniques for TV-Band White-Space Wireless Transmitters, 2014

Macao-fellow, University of Macau, Macao

Chi-Hang CHAN

Design Techniques and Considerations in Moderate to Low Resolution Power efficient GHz Range ADCs, 2015

Research Assistant Professor, University of Macau, Macao

U-Fat CHIO

Design Techniques for Low-Power High-Speed Analog-to-Digital Converters using Binary-Search and Subranging Schemes, 2012 Post-Doc, University of Macau, Macao

Yan ZHU

Circuit Techniques for High-Performance SAR-Type ADCs, 2011

Assistant Professor, University of Macau, Macao

Hegong WEI

High Speed Power/Area Optimized Multi-Bit/Cycle SAR ADCs, 2011

Engineer, Silicon Lab, USA

THESES AWARDED

M.Sc. (51)

Yuan REN

On the study of High DR Multi-Channel Stage-Shared Hybrid Front-End for Integrated Power Electronics Controller, 2016
Research Assistant, University of Macau, Macao

Wei LI

On the study of Mixed Signal Interface Circuit for Inertial Navigation System, 2016

Wen-Liang ZENG

Design and control of an integrated ky dc-dc step-up converter under dcm operation, 2016
Ph.D., University of Macau, Macao

Sut-Ian HO

Study of a combined system of thyristor controlled LC filter and hybrid active power filter, 2016
Facilitator of Telecommunication Exhibition, Communications Museum of Macao, Macao

Sanfeng LU

High Accuracy Multi-Range Ultra-Low Power/Non-Calibrated CMOS Smart Temperature Sensor Design, 2016
Design Engineer, Jia Chi Microelectronics Technology, China

Haojuan DAI

A Wide Input Range CMOS Rectifier Design for RF Energy Harvesting, 2016
Design Engineer, Solomon Systech, China

Baoning ZHANG

Cross-Correlation Phase Counting Method Using FPGA Implementation for Digital Frequency Domain Fluorescence Lifetime Imaging Microscopy, 2016
IT Engineer, Macau Tai Fung Bank, Macao

Haodong YAO

Novel Enveloped-Morphology Heart Sound Feature Extraction for Intelligent Cardiovascular Disease Prognosis in e-Home Healthcare, 2016

Wai-Kei TOU

Improved Motion Control System on Elevator Landing and Re-leveling, 2016

Haiyang WANG

Diagnose CVDs through Detecting Heart Sounds, 2016

FeiYUAN

A 10b Pipelined ADC with Nonlinear Digital Background Calibration & 2.5b/stage Opamp Sharing Architecture, 2015
Design Engineer, Synopsys Macau, Macao

Suyan FAN

AA Wide-Input-Range Supply Voltage Tolerant Capacitive Sensor Readout Using On-Chip Solar Cell, 2015
Design Engineer, Allwinner Technology, China

Yue LI

Joint-Digital-Predistortion for Wireless Transmitter's I/Q Imbalance and PA Nonlinearities Using an Asymmetrical Complexity-Reduced Volterra Series Model, 2015
Ph.D., Ryerson University, Canada

Choi-Fong NGAI

Predicting Protein Docking Poses on a Solid Surface by Particle Swarm Optimization: a Case Study of Lysozyme on PTFE, 2015
Ph.D., University of Macau, Macao

Wang MENG

Towards Improving Performance of Galvanic Coupling Intra-Body Communication, 2015

Cao-Xue WEN

Acoustic Output Pressure Enhancement for CMUTs Using Helmholtz Resonance Principle, 2015

Huijuan HUANG

FPGA Implementation of Synthetic Aperture Algorithm for Ultrasound Imaging, 2015

Mubo CHEN

Disease-Oriented Hierarchical CVDs Diagnosis Hemodynamic, Symptomatic and Physiological Parameters, 2015

Chon-In LAO

Semi-MASH Sigma-Delta A-to-D Converter for High-Speed High-Resolution Applications, 2014
Design Engineer, Synopsys Macau, Macao

Jiangchao WU

An Ultra-Low Power Acquisition Front-End for Neural Recording, 2014
Ph.D., University of Macau, Macao

M.Sc. (CONTINUED)

Xingqiang PENG

Advanced Circuit Techniques for Class-D Power Amplifiers in Nanoscale CMOS, 2014

Ph.D., University of Macau, Macao

Wei WANG

Systematic Analysis and Design of Area-Efficient Frequency Compensation Techniques for Micropower Two-Stage CMOS Amplifiers, 2014

Ming-Zhong LI

Sub-threshold Standard Cell Library Design for Ultra-Low Power Biomedical Applications, 2014

Ph.D., University of Macau, Macao

Yan-Zheng YANG

Design and implementation of a mixed signal controller for power quality compensator improvement, 2014

Project Manager, China National Water Resource & Electric Power Materials & Equipment Co, Ltd., China

Bo JIN

Law Detection and Localization in Steel Material by the EEMD-HSA Based Technique in Ultrasonic Nondestructive Testing, 2014

Du YUN

On the Study of Improving Noise Shaping Techniques in Wide Bandwidth Sigma Delta Modulators, 2013

Wenlan WU

Monotonic Multi-Switching Method for Ultra-Low-Voltage Energy Efficient SAR ADCs, 2013

Engineer, Alphacore Inc., USA

Cheok-Teng LEI

Applying the Novel High Speed Robust Level Converter to a 12-bit Successive Approximation Analog-to-Digital Converters with Dual Supply Domain, 2013

Design Engineer, Synopsys Macau, Macao

Chenyan CAI

On the Study of Excess-Loop-Delay Compensation Techniques in Continuous-Time Delta-Sigma Modulators, 2013

Research Assistant, University of Macau, Macao

Tao WU

An Ultra-Low Power CMOS Smart Temperature Sensor for Clinical Temperature Monitoring, 2013

Technical Consultant, Huada Emphyrean, China

Gengzhen QI

Design and Optimization of Wideband Power Amplifier and its Driver Circuits, 2013

Ph.D., University of Macau, Macao

Tianlan CHEN

Design and Implementation of Intelligent Digital Microfluidics Systems, 2013

Ph.D., University of Macau, Macao

Zhiyuan CHEN

Single-Chip Solar Energy Harvesting IC with Integrated Solar Cells for Biomedical Application, 2013

Ph.D., University of Macau, Macao

Yanjie XIAO

Capacitance-to-Digital Convert for Lab-on-Chip Digital Microfluidics Systems and Ultra-Low-Power SAR ADC with Effective Calibration, 2012

Senior Engineer, Schaeffler, China

Weng-Fai CHENG

A Wideband Balun Low-Noise Amplifier and Digital Impairment Correction Techniques for Wireless Transmitters, 2012

Engineer, CTM, Macao

Wei-Han YU

Digital Predistortion and Calibration Techniques for Wireless Transmitters, 2012

Ph.D., University of Macau, Macao

Jiang YANG

On the Study of Clock-Jitter Insensitive Circuit Techniques in Continuous-Time Sigma-Delta Modulators, 2012

Ph.D., University of Macau, Macao

Zhijie CHEN

Ultra-Low Power Sigma-Delta ADC for Biomedical Readout Front-End, 2012

Assistant Professor, Beijing University of Posts and Telecommunications, China

THESES AWARDED

M.Sc. (CONTINUED)

Wang RUI

Digital Calibration Techniques for Cyclic Analog-to-Digital Converter, 2012
Ph.D., University of Idaho, US

Peng ZHANG

Calibration of Timing-Skew error in Analog-to-Digital Converter, 2012
Engineer, Marvell Semiconductor, China

Wai-Hei CHOI

Low Loss and Dynamic Reactive Power Compensation Control Algorithms for Three-Phase Four-Wire Hybrid Active Power Filters, 2012
Laboratory Technician, University of Macau, Macao

Xiao-Xi CUI

Control and Minimization of DC-Link Voltage for Three-Phase Four-Wire Hybrid Active Power Filters, 2012
Research Assistant, University of Macau, Macao

Chong-Yin FOK

Very Short Term Load Forecasting for Macau Power System, 2012

Dong CHENG

Electrocardiogram Parameter Extract and Analysis System, 2011
Ph.D., University of Macau, Macao

Mia LIU

High-Voltage-Enabled Operational Amplifier and Active-Decoupling Technique for Wideband Balun-LNA, 2011
Engineer, STMicroelectronics, China

Cheng-Man NG

Electroencephalogram Analysis Based on Empirical Mode Decomposition, 2011

Chin-Chong HO

From Vehicle Black Box System to Elevator Event Data Recorder, 2011

Guohe YIN

Ultra-Low Power SAR ADC for Biomedical Application, 2011
Chinese Academy of Sciences, China

Chi-Hang CHAN

A Study on Comparator and Offset Calibration Techniques in High Speed Nyquist ADCs, 2011
Ph.D., University of Macau, Macao

Si-Seng WONG

Design of Analog-to-Digital Converters with Binary Search Algorithm and Digital Calibration Techniques, 2011
Design Engineer, Synopsys Macau, Macao

Bo SUN

A FPGA-Based Power Electronics Controller for Three-phase Four-wire Hybrid Active Power Filters, 2011